

Fig. 1

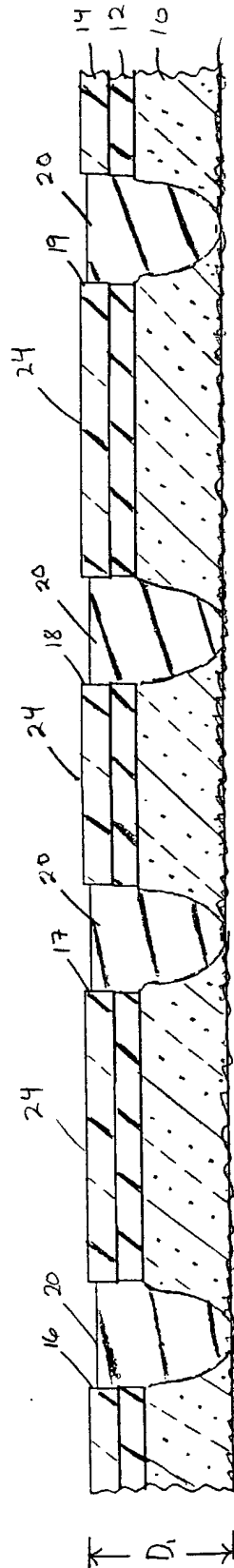


Fig. 2

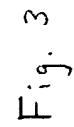
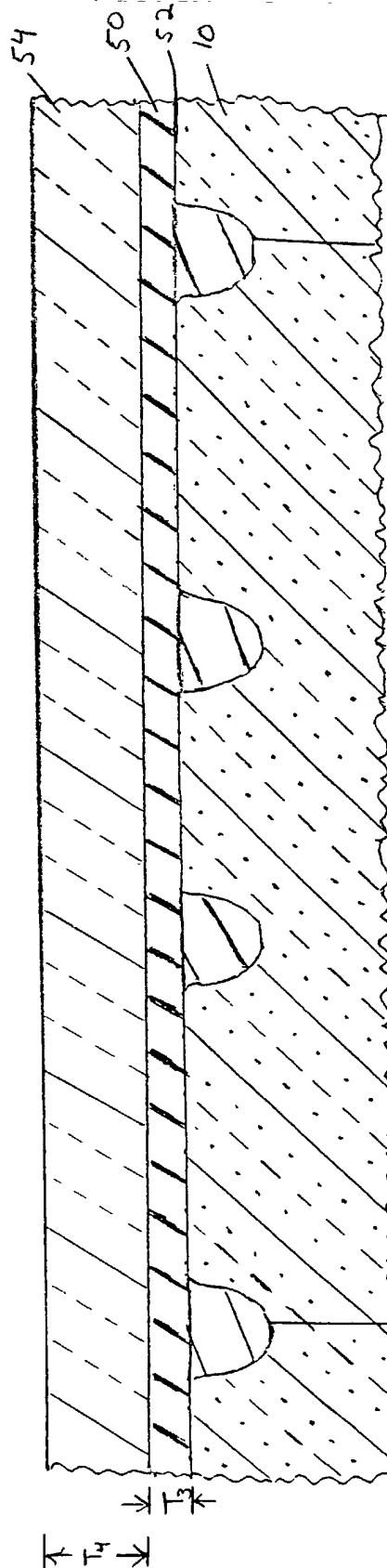


Fig. 3



Applicant(s): Chen-Yih Jin et al.

NON-SILICON SEMICONDUCTOR AND HIGH-K GATE
DIELECTRIC METAL OXIDE SEMICONDUCTOR FIELD
EFFECT TRANSISTORS

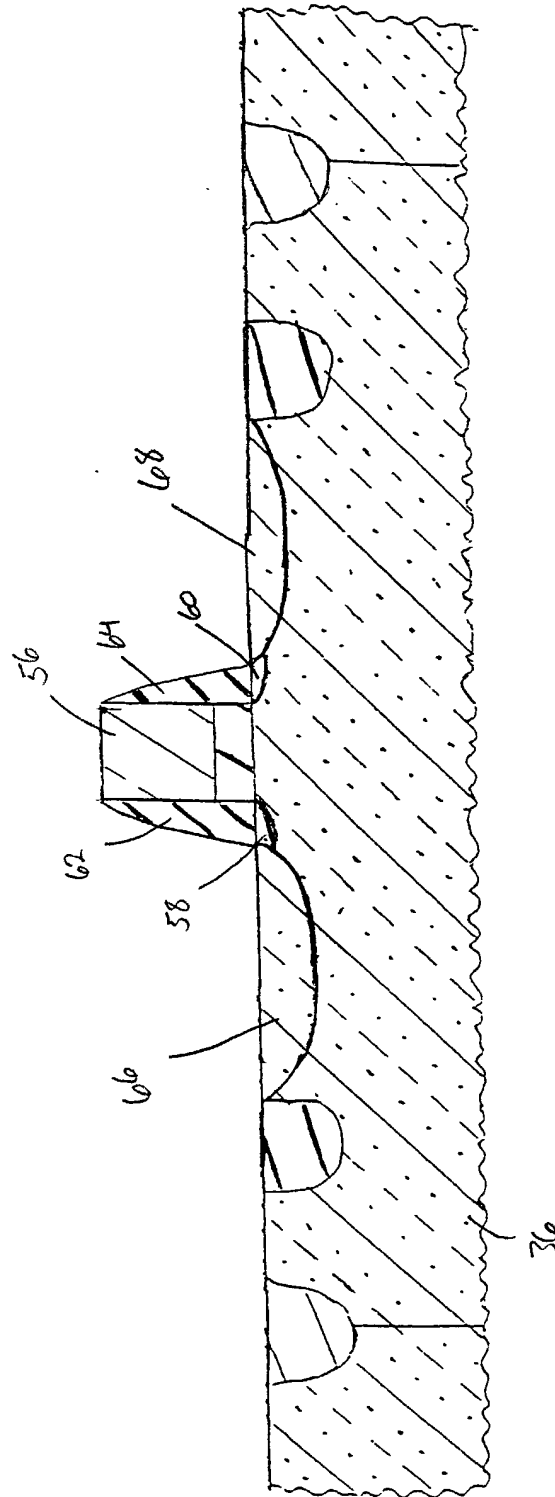


Fig. 5

